

Fig. 1

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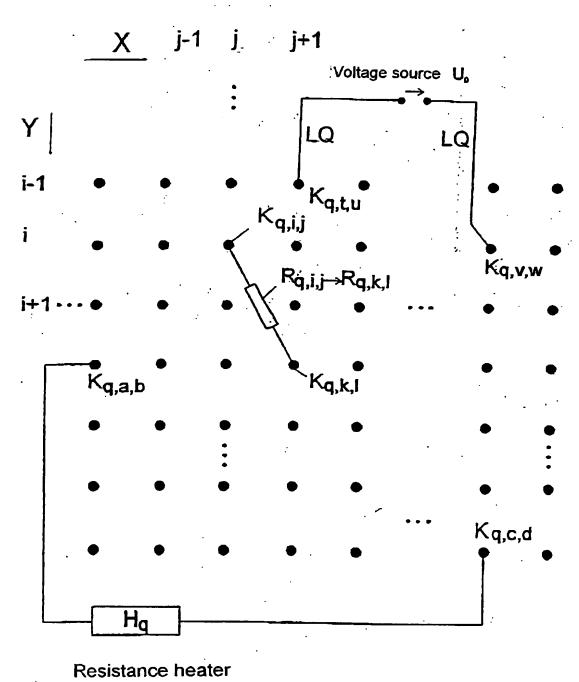
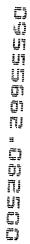
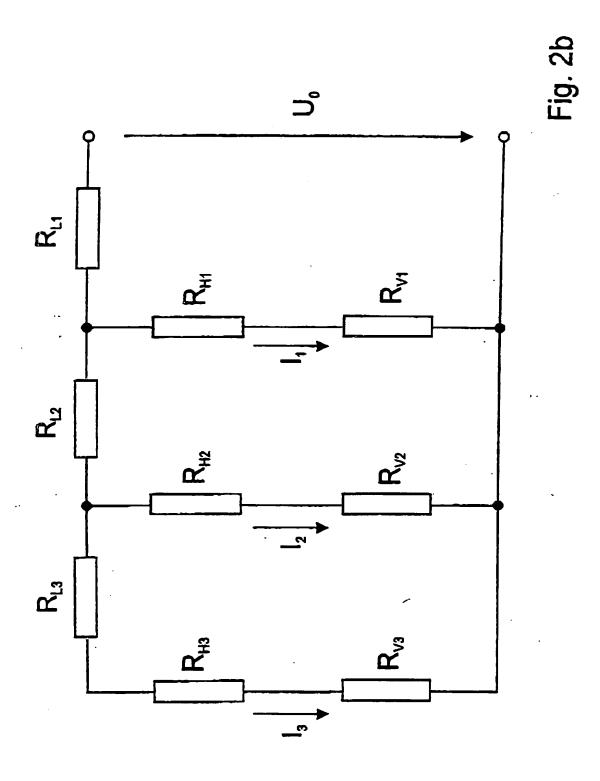
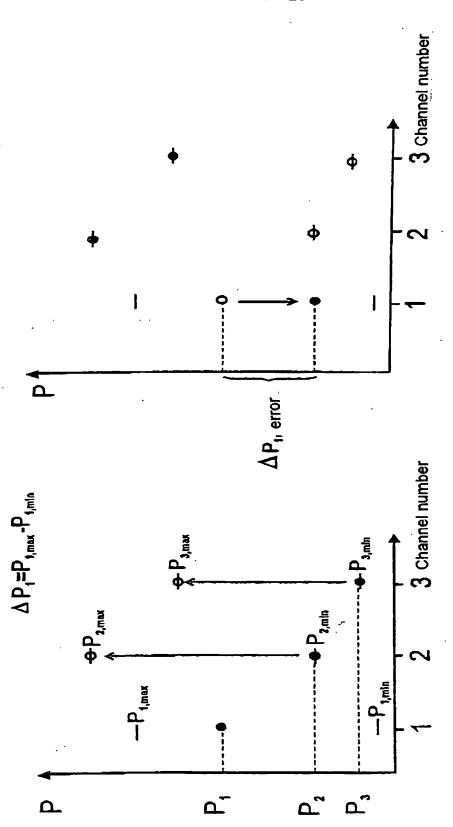


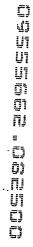
Fig. 2a

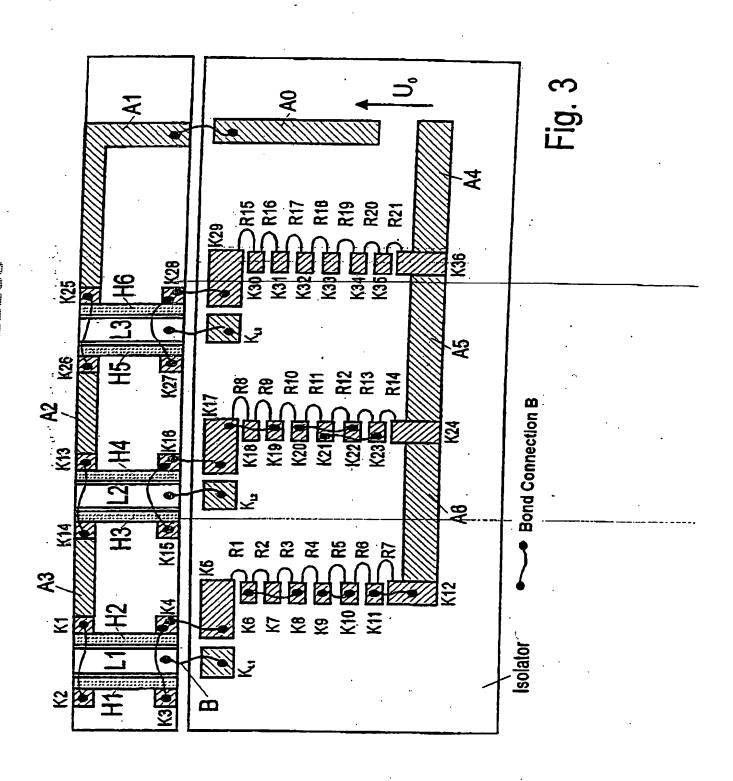




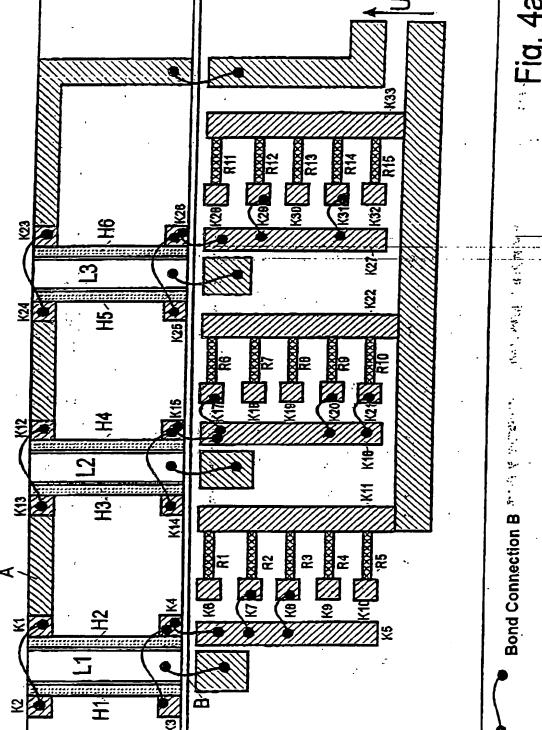
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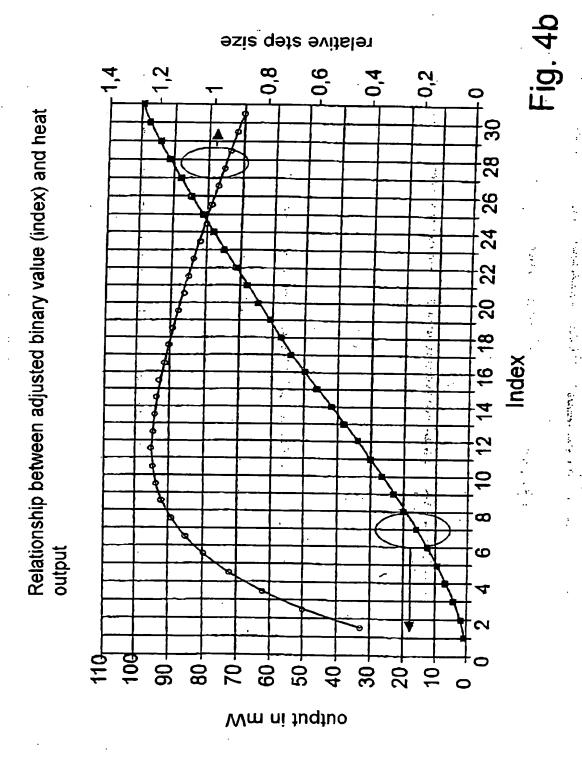






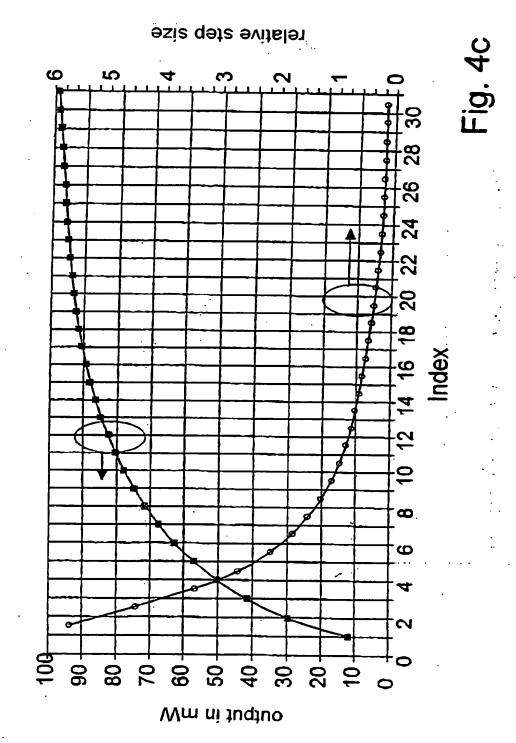
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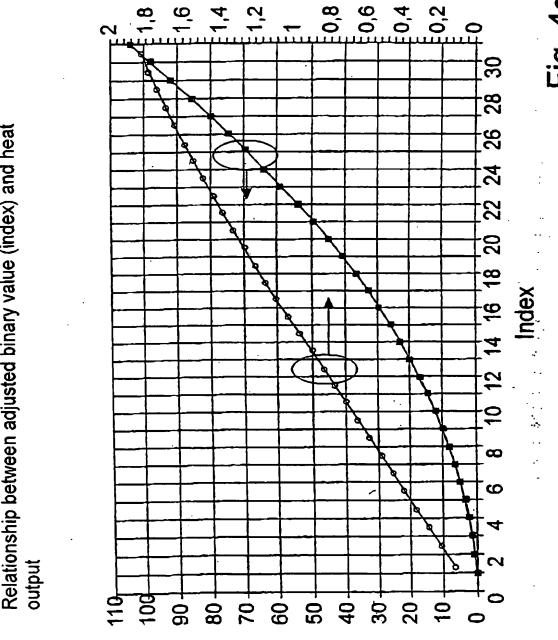
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relative step size

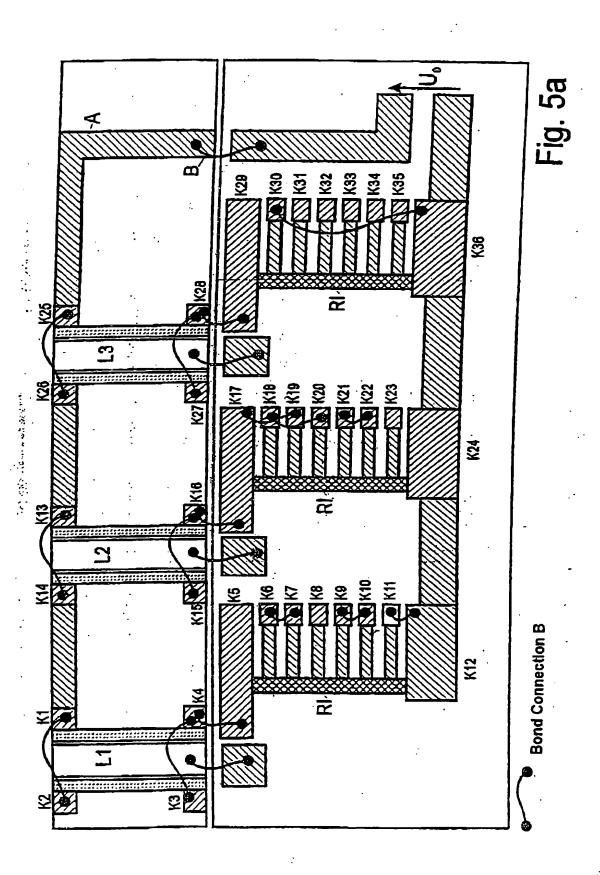
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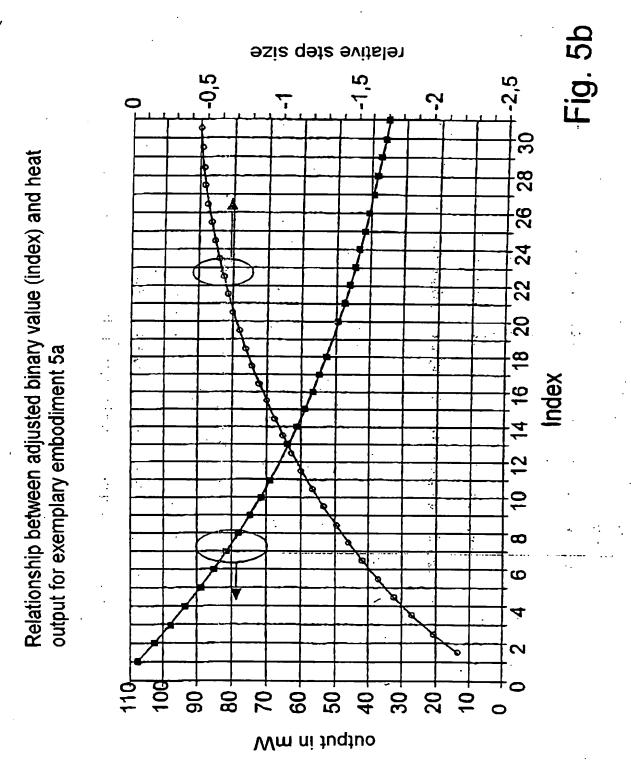
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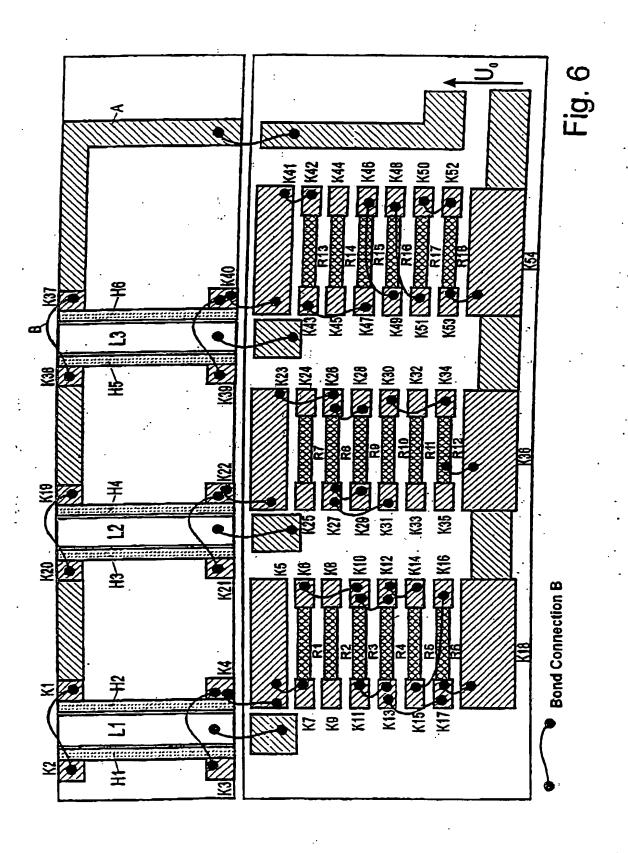
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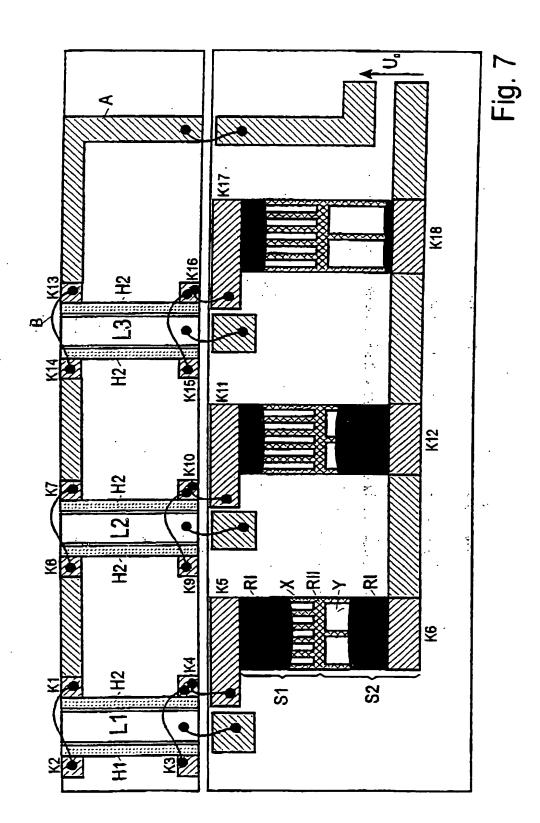
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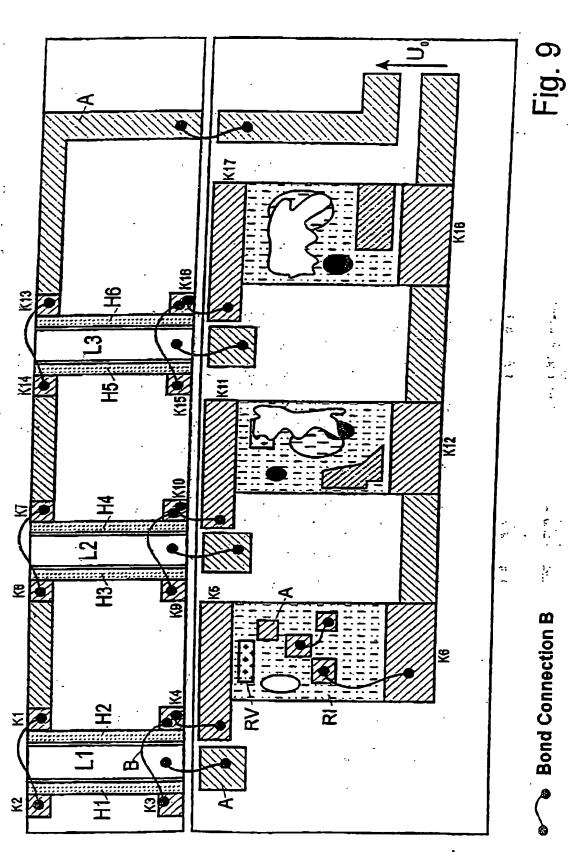


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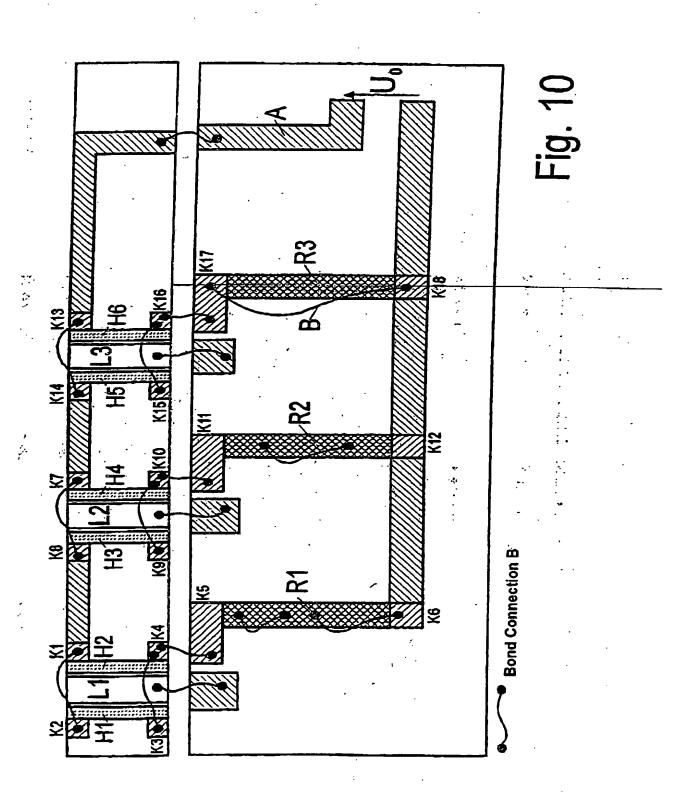


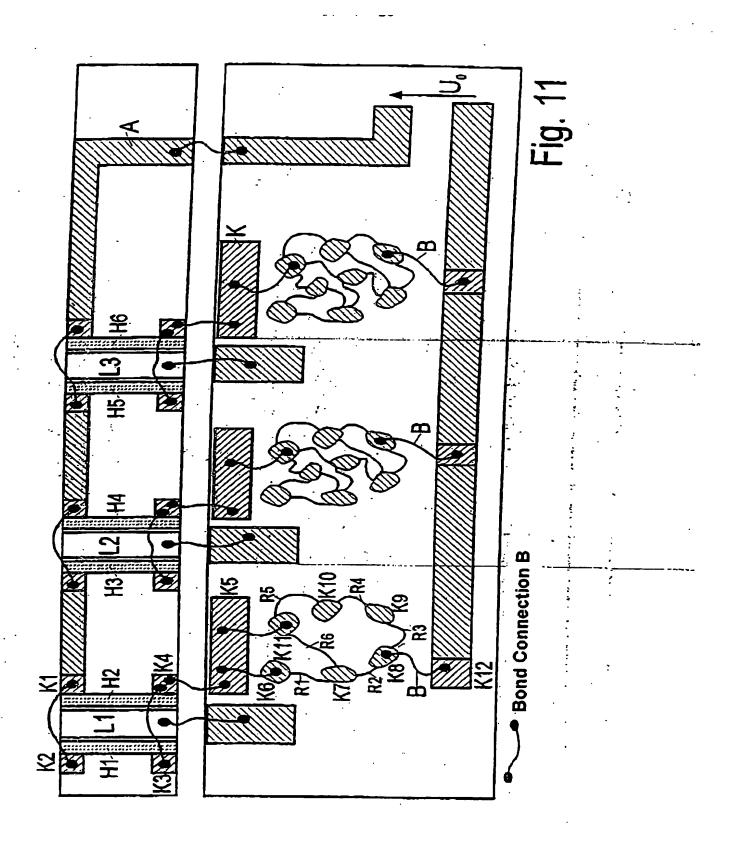
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Fig. 8



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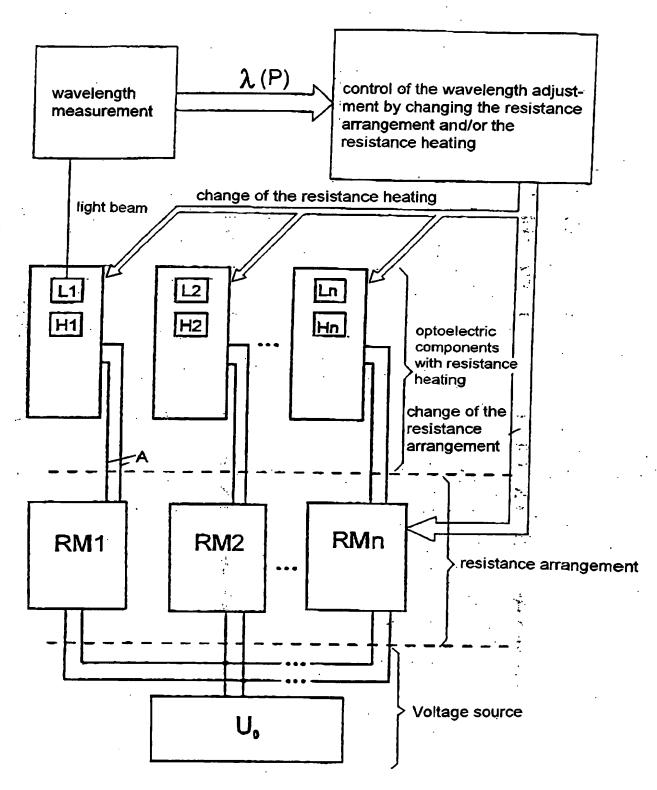
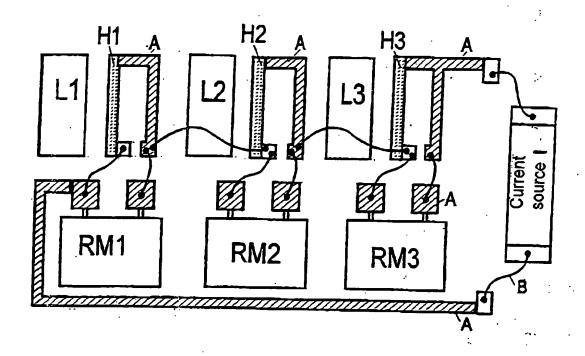


Fig. 12b

Exemplary embodiment including current source



electrical circuit for exemplary embodiment

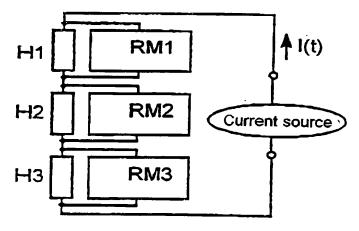


Fig.13